

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Priority** Application Serial No. .... 09/139,691  
**Priority** Filing Date ..... August 25, 1998  
Inventor..... Michael J. Hermes  
Assignee..... Micron Technology, Inc.  
**Priority** Group Art Unit ..... 2812  
**Priority** Examiner ..... Pompey, Ron E.  
Attorney's Docket No. .... MI22-2341  
Title: Semiconductor Processing Methods of Forming Dynamic Random Access Memory  
(DRAM) Circuitry

**PRELIMINARY AMENDMENT ACCOMPANYING CONTINUATION APPLICATION**

To: Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

From: Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828

**AMENDMENTS**

**Introductory Comments**

The present Preliminary Amendment is submitted in the Revised Amendment Format and therefore qualifies for waiver of provisions 37 CFR 1.121(a), (b), (c) and (d).